

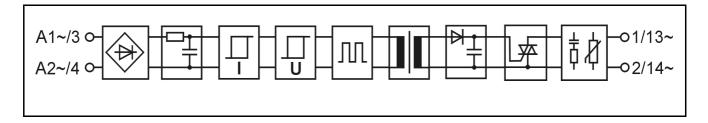
#### SLO P120TR

SL-series plug-in output relay

#### **Main features**

- Solid state output relay
- cULus Listed, CE (EMC and LVD)
- Integrated status LED
- For static AC-loads
- Proximity switch compatible input

# Functional block diagram



### Main specifications

Breakdown voltage I/O	minimum	4300	VAC rms	
Air/creepage distances I/O	minimum	8	mm	
Capacitance I/O	typical	3	pF	
Material of the casing	PBT	UL 94V-0		
Colour of the casing		Black		
Weight	typical	40	g	
Temperature range:				
Storage	range	-40+70	∞	
Operation	range	-25+70	℃	

# Electrical specifications ( $T_A = 25$ °C)

Primary				Secondary			
Input voltage	nominal	120	VAC	-	minimum	0	VAC
Input current at	typical	5	mA	Load voltage	nominal	240	VAC
nominal voltage	maximum	6	mA		maximum	265	VAC
Input voltage	minimum	95	VAC	Load current	maximum	3	Α
range (abs.)	maximum	140	VAC	Load current	maximum	90	A (20 ms)
Input impedance	typical	24	kΩ	Voltage drop	typical	1	V
Switch-on voltage	typical	80	VAC	Switch-on delay	typical	10	ms
	maximum	95	VAC		maximum	-	ms
Switch-off voltage	typical	60	VAC	Switch-off delay	typical	20	ms
Switch-on voltage	minimum	45	VAC		maximum	-	ms
Switch-off current	typical	3	mA	Load power factor, cos Φ		01	
Cable	typical	68	nF	dv/dt, off-state	typical	200	V/μs
capacitance				Leakage current (off-state)	typical	1,5	mA

Cable capacitance is a capacitance which is not keeping the relay in on-state (typical value at 120 VAC) or total current 3 mA (typical). Ambient temperature ( $T_A$ ) means the temperature immediate in vicinity of relays, where the air flow meets the relays.

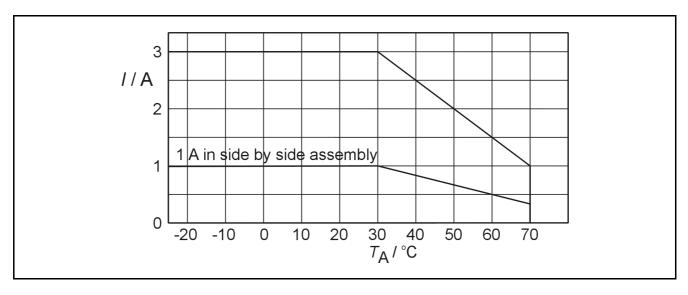


#### Limitations

Ambient temperature (T <sub>A</sub> )	Limitations
-25 °C+40 °C	Allowed maximum current is 33 % of the curve below when
	assembled side by side.
+40 °C+55 °C	Only every other relay should be in on-state at current which is
	33 % or less of the curve below when assembled side by side.
+55 ℃+70 ℃	If relays are most of the time on, there should be a gap in both
	sides at least 12,5 mm. Notice also the curve below.

### **Deratings**

Allowed load is derated to 1/3 linearly from +30  $^{\circ}$ C to +70  $^{\circ}$ C ambient temperature. Derating curve for the relay when there is at least 12,5 mm gap between relays. These deratings apply when assembled to the horizontal rail. If assembled to the vertical rail, must be taken care that the relays do not heat up too much.



Derating curve for SLO P120TR.

### **Derating when switching inductive loads**

There is no need to derate solid state output relay using a triac switch. The relay is indifferent to the power factor of the load. Calculation should be made however that the surge current does not exceed the specification. For reasons of heat dissipation, when the load will be switched frequently, the average current over a reasonable time should not exceed the specification for continuous operation.

#### **Fusing**

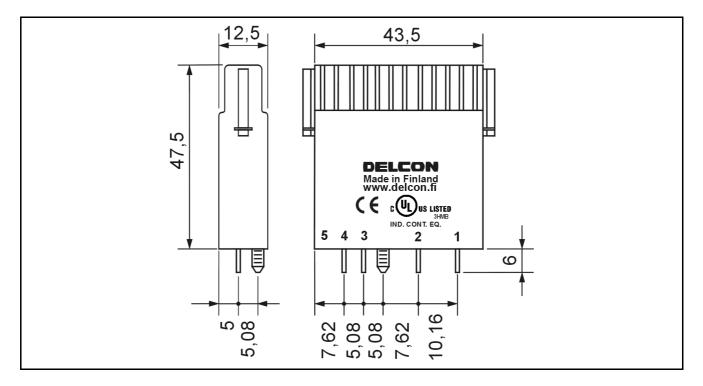
To protect relay against short circuit and overload a fast fuse with the correct rating for the load and the capacity of the relay should be chosen. Note that when overload current is not large it is possible that the fuse will not protect the relay because of the tolerance on the fuse rating.

#### Assembling

All MOS 1... -mounting sockets. The recommended installation is to the horizontal rail for better cooling of the relays.



#### **Mechanical dimensions**



SLO-relay (plug-in), dimensions in mm, nominal.

# **Approvals**

CUL US LISTED 3HMB IND. CONT. EQ.	Certificate: E162828
CE	Fulfils main requirements of the EMC-directive 2004/108/EC. Fulfils requirements of the low voltage directive (LVD) 2006/95/EC.

### Guarantee

This solid state I/O relay type made by Delcon Oy is guaranteed free from design and manufacturing defects for a period of 10 years from the manufacturing date. The guarantee liability is limited to replacement of defective material and related shipping charges. Defective products must be returned to the manufacturer for evaluation. This guarantee does not cover damage due to incorrect use or electrical overload.